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f. Foreign Priority

b. Applicant(s)	g. Disclaimer	I. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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	Complete if Known	·
Application Number	10/067,151	
Filing Date	January 29, 2002	
First Named Inventor	Rupp, Charle'	
Art Unit	2825	
Examiner Name	Do, Thuan V.	
Attorney Docket Number	PA2194HS	

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		JOHN HESKETH, "The Programmable Logic Core: Enabling the configurable System-On-a-Chip", Technical Presentation by LSI Logic Corporation, presented 1/30/2001in US	
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